

ABSTRACT OF THE DISCLOSURE

An error correction apparatus includes a receiving section that receives a signal. An amplifier section amplifies the signal received by the receiving section. An automatic gain control section generates a control signal and controls the gain of the amplifier section based on the generated control signal so as to maintain the amplified received signal at a predetermined level. A turbo decoder is configured to execute an error correction process on the received signal to thereby output a decoding result of the executed received signal, based on the generated control signal.

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